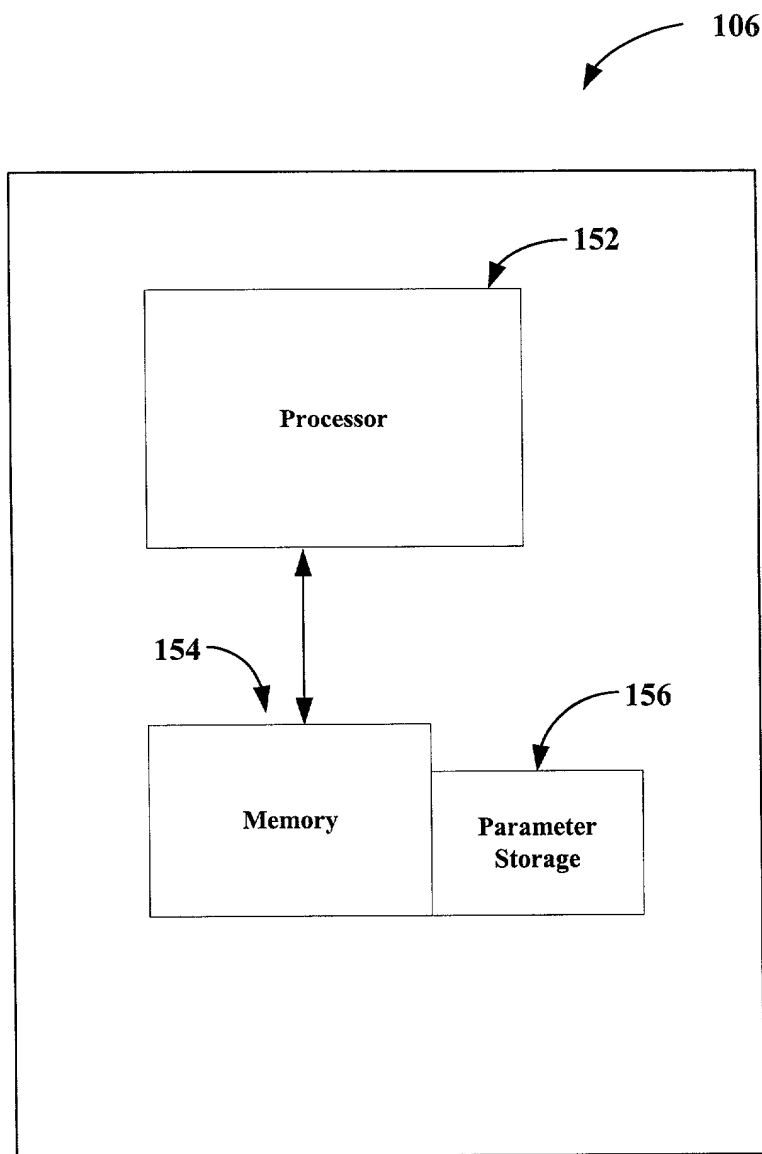
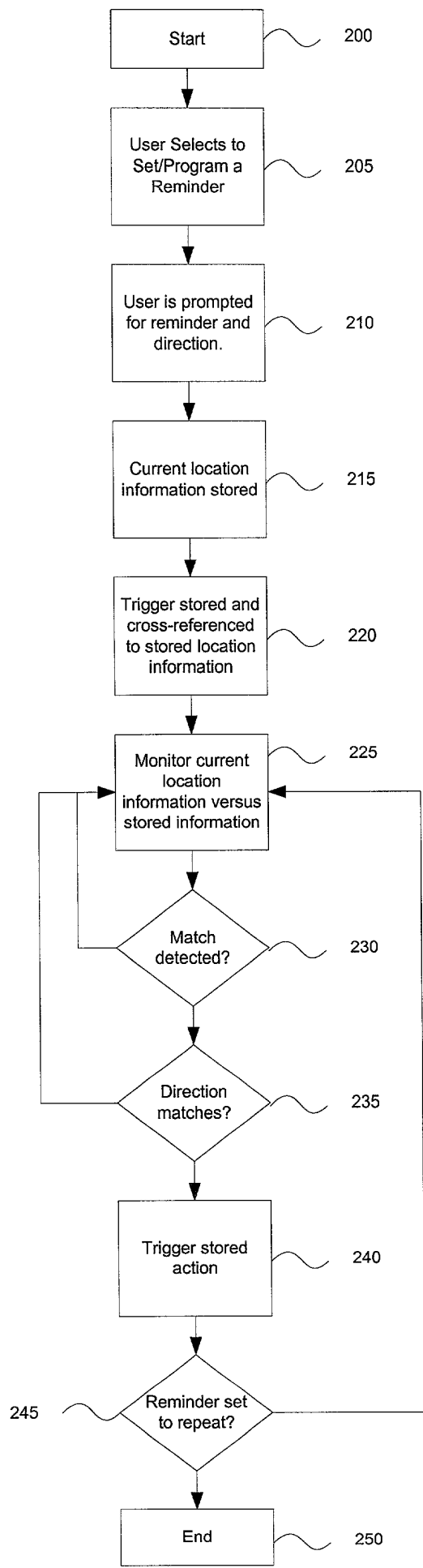


**Figure 1A**

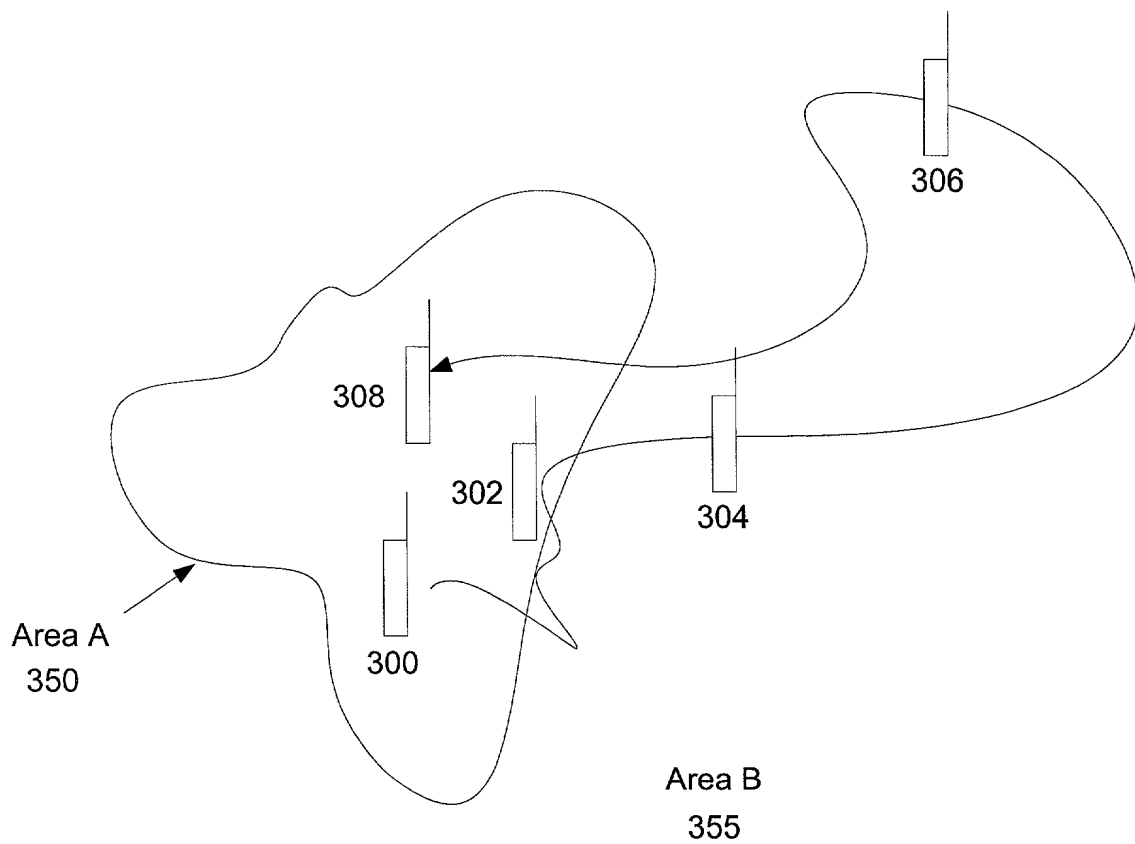
FIG. 1B is a block diagram of a system 106. The system 106 includes a processor 152, a memory 154, and a parameter storage 156. The processor 152 is connected to the memory 154 and the parameter storage 156. The memory 154 and the parameter storage 156 are connected to each other.



**Figure 1B**



**Figure 2**



**Figure 3**

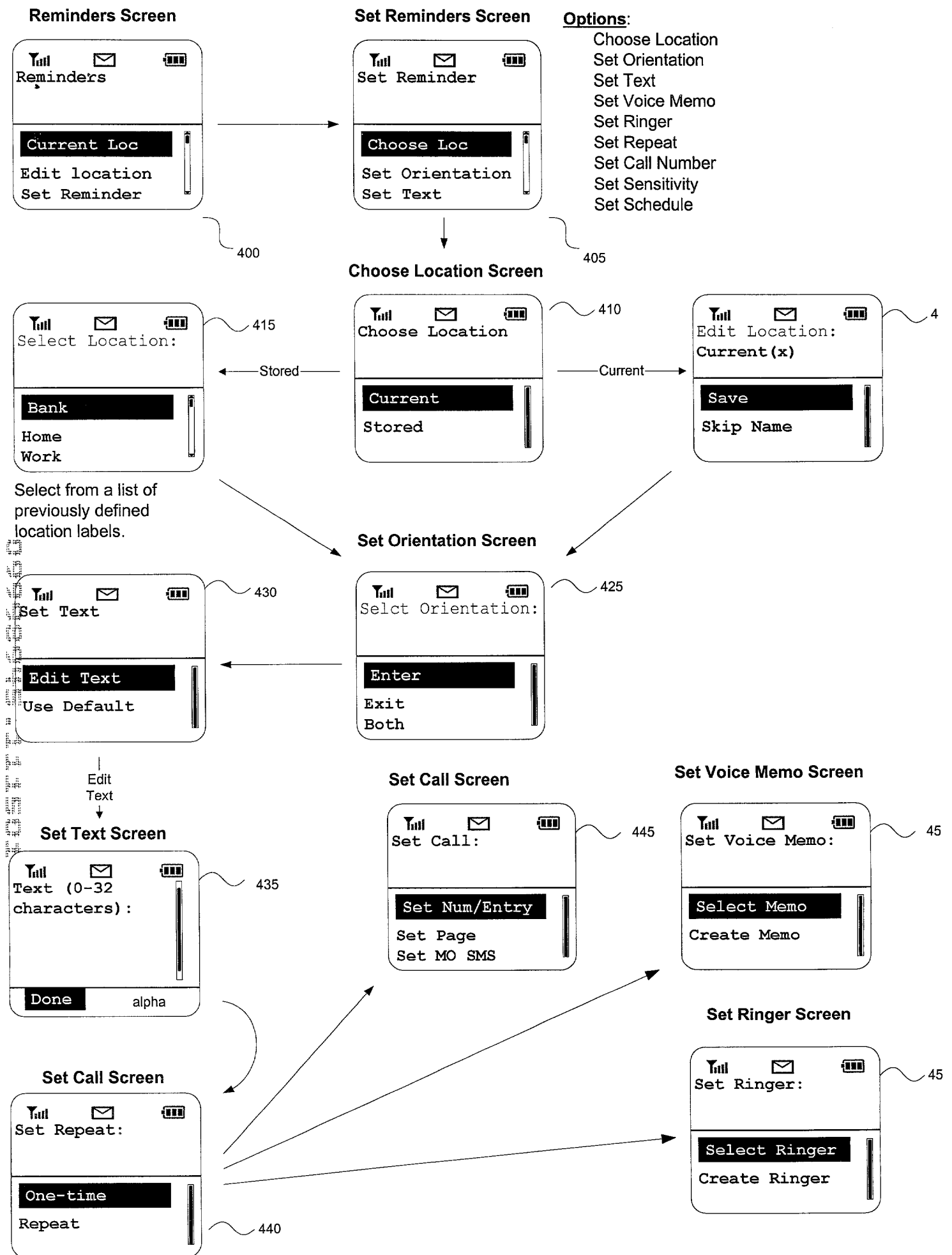


Figure 4